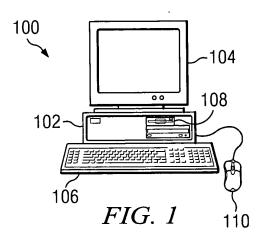
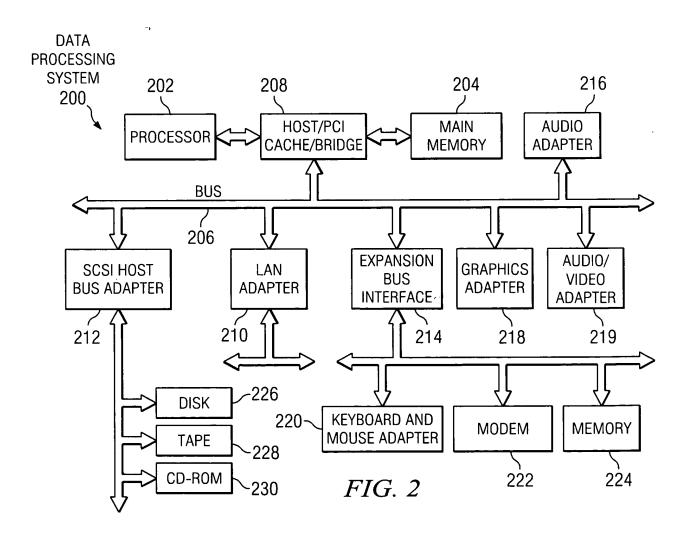
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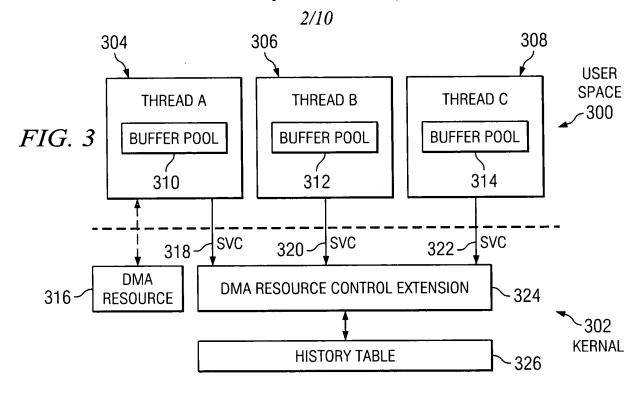
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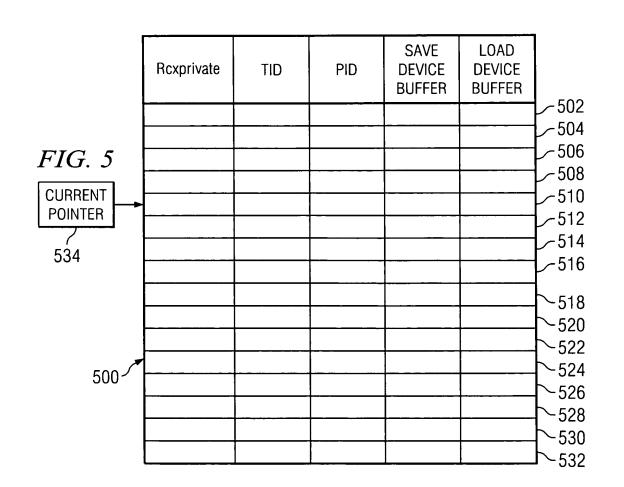




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# DMA REQUEST CHAIN (USER QUEUE)

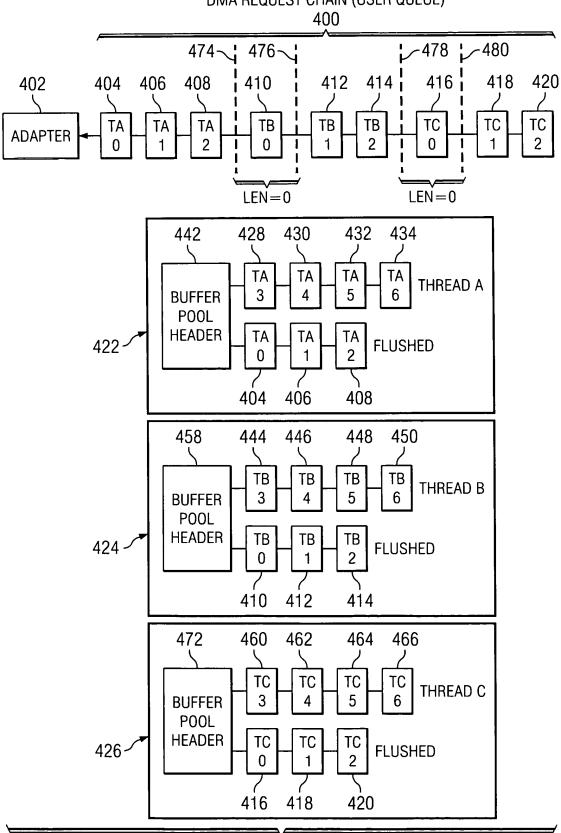
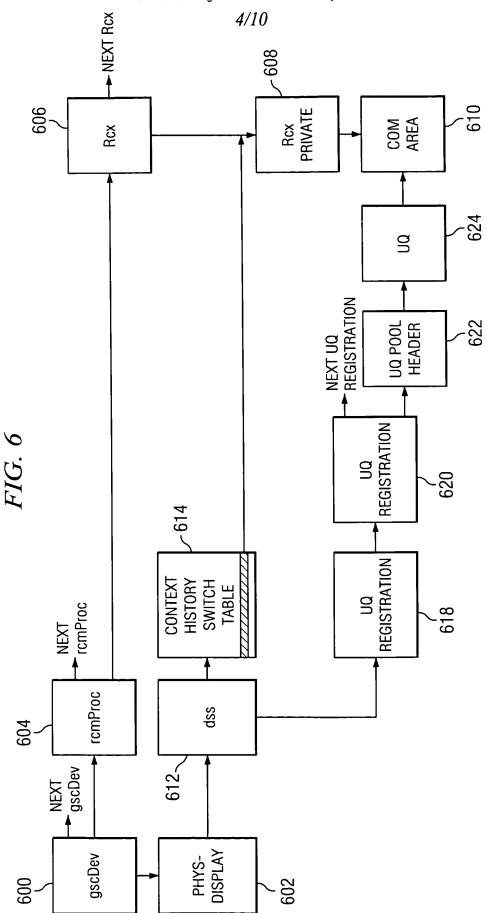


FIG. 4

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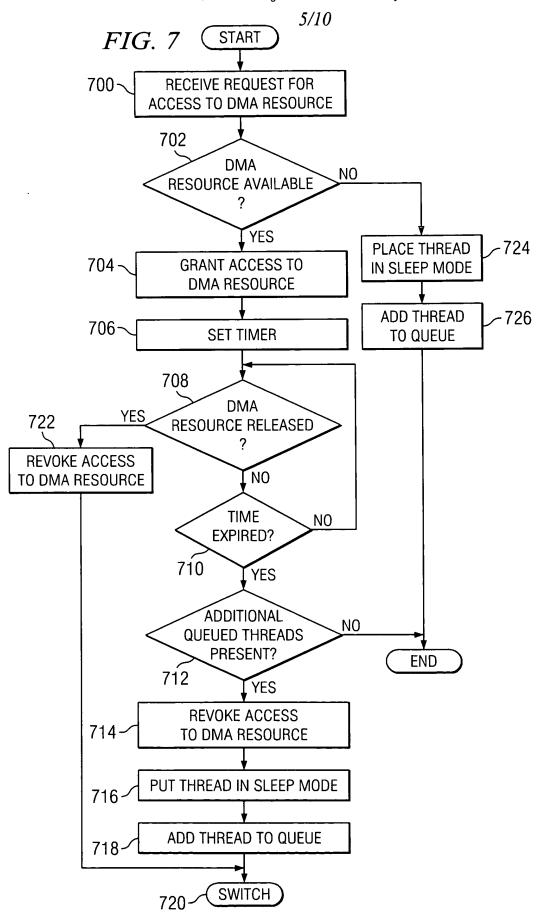
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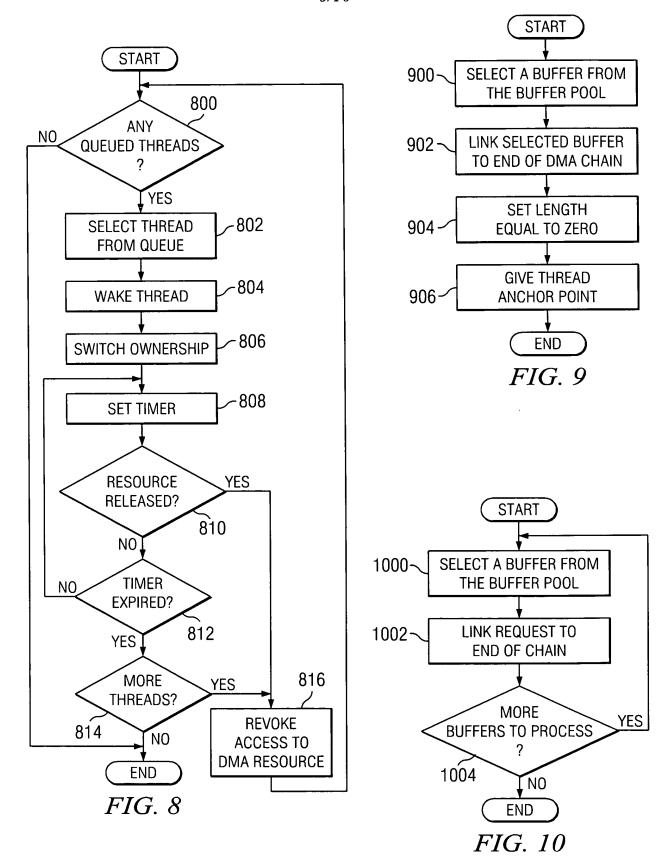
7.7

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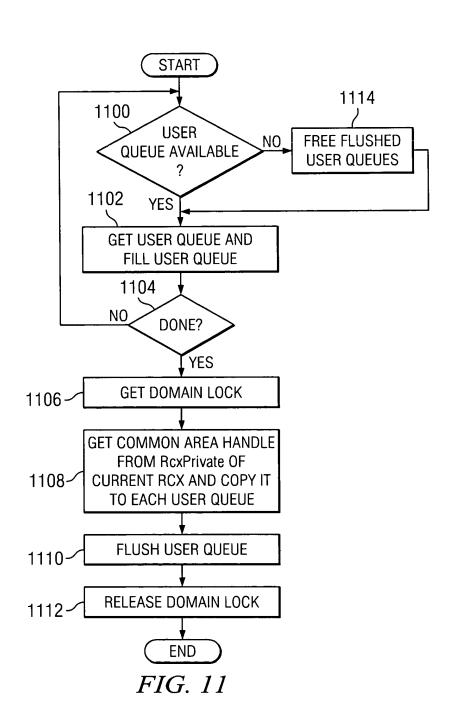
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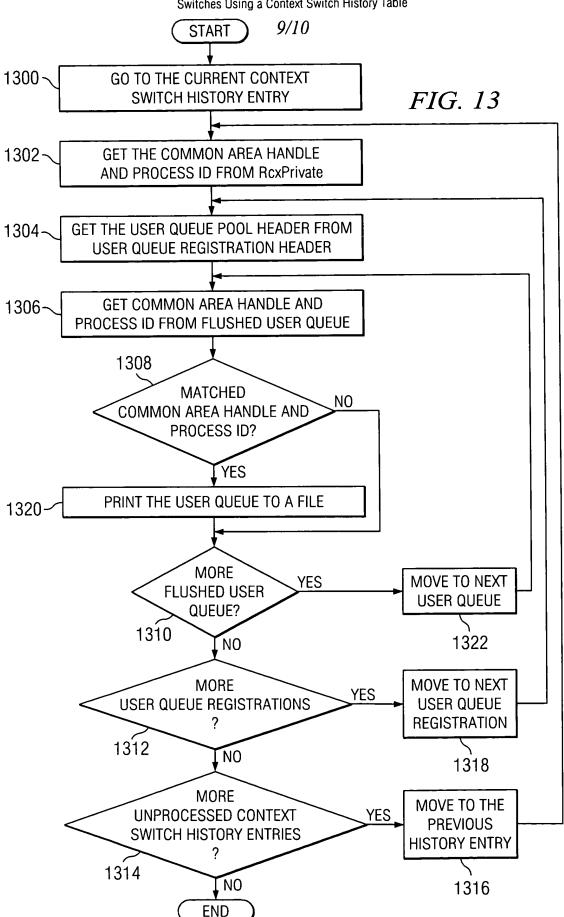


## Kuo et al. Method and Apparatus for Managing Context Switches Using a Context Switch History Table 8/10 **START** 1200 **DMA** NO **ENGINE RUNNING** 1222 YES 1202 **GET USER QUEUE** GET DMA CONTROL BLOCK REGISTRATION HEADERS BEING PROCESSED BY ADAPTER FROM DEVICE SPECIFIC **STRUCTURE** 1204~ GET USER QUEUE REGISTRATION AND LOCATE USER QUEUE POOL THAT OWNS THE DMA CONTROL BLOCK FREE ALL FLUSHED **USER QUEUE** FREE ALL PROCESSED USER QUEUES ON THE 1206 FLUSHED LIST AND RETURN THE COMMON AREA 1220 HANDLE FROM FREED USER QUEUE IS FOUND SCAN CONTEXT SWITCH HISTORY TABLE 1208 BACKWARDS UNTIL A MATCHING COMMON AREA HANDLE FROM FREED USER QUEUE IS FOUND STARTING WITH CONTEXT ENTRY PRIOR TO CURRENT ENTRY, RUN BACKWARDS 1210 THROUGH CONTEXT SWITCH HISTORY TABLE MATCH PROCESS ID AND COMMON AREA HANDLE FROM CONTEXT SWITCH ENTRY WITH FLUSHED USER 1212-QUEUE'S FROM USER QUEUES REGISTRATIONS FREE THE MATCHED USER QUEUE 1214 AND MOVE TO THE NEXT USER QUEUE NO DONE? YES 1216 1224 **MORE** MOVE TO NEXT UNPROCESSED YES **UNPROCESSED CONTEXT SWITCH HISTORY ENTRY ENTRIES** 1218 NO FIG. 12 **END**

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